

Senior Engineer, ASIC Verification

Kionix, Inc., a global leader in the design and fabrication of high-performance sensors, has an immediate opening for a Senior Engineer, ASIC Verification. This position is not eligible for visa sponsorship. Must have unrestricted authorization to work in the U.S.

Education/Training

- B.S. or M.S. in Electrical Engineering

Key Requirements

- Minimum of 5 years experience in the verification and simulation of complex digital or mixed signal integrated circuits
- Experience in design and verification of digital integrated circuits in CMOS foundry processes
- Knowledge of Verilog and System Verilog in a normal coding, synthesis, and verification environment
- Expertise in developing comprehensive test plans based on product specifications and implementing in a highly structured environment
- Able to understand verilog code, debug simulation and coding errors, and identify fixes based on simulation feedback

Key Responsibilities

- Project leadership role in the verification and simulation of digital integrated circuits by translating product specification to a comprehensive verification environment
- Develop system verilog test methodology for automated testing and checking of digital and mixed signal circuits
- Define and develop reusable verification environments across multiple similar projects
- Planning and resourcing of verification within a broad mixed signal design effort

***Kionix, Inc. is proud to be an Equal Opportunity
and an Affirmative Action Employer***